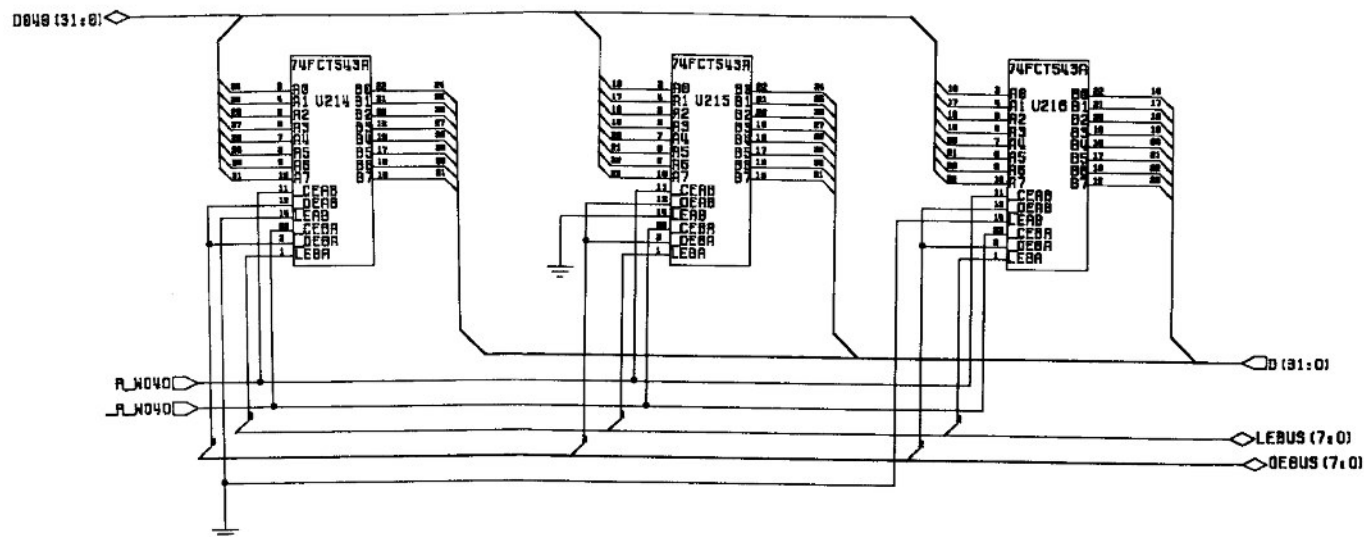
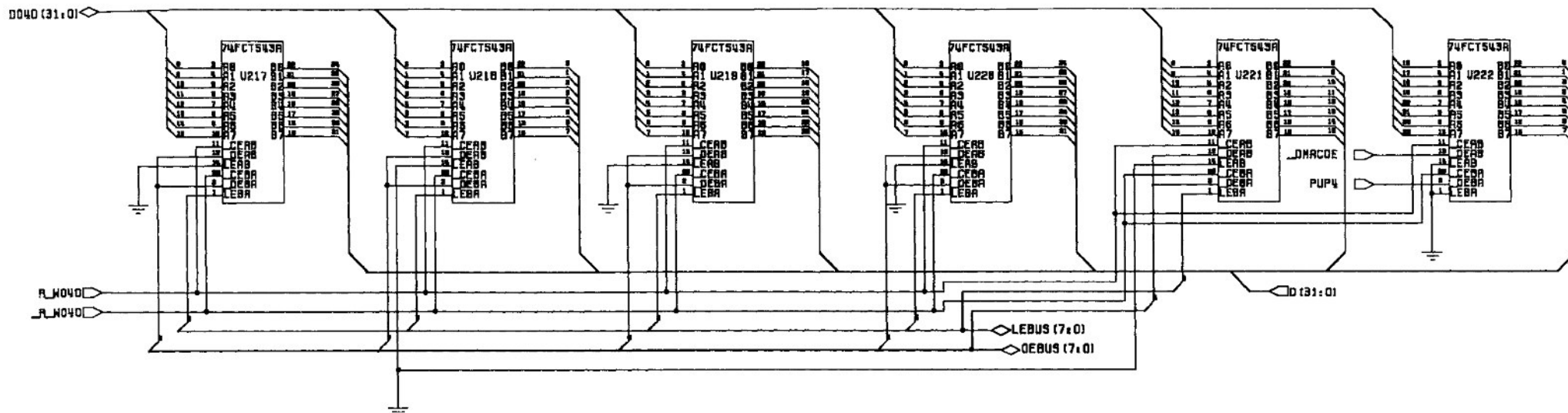
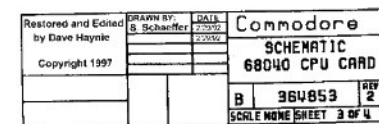
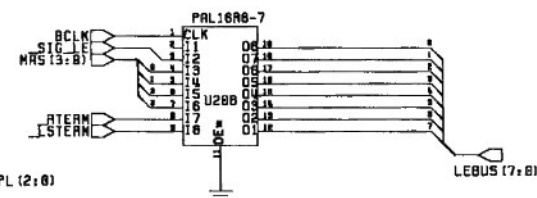
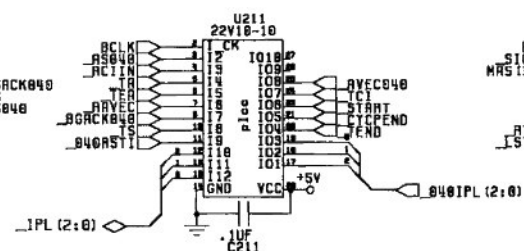
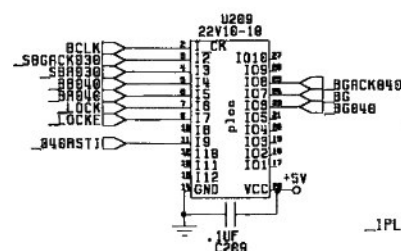
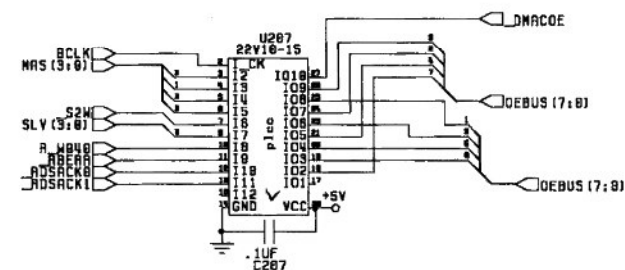
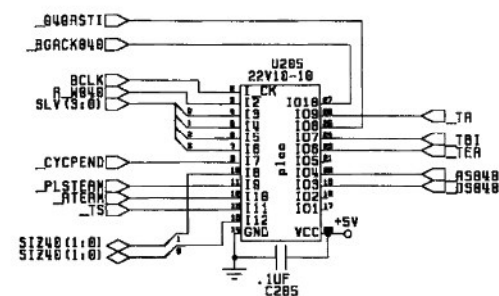
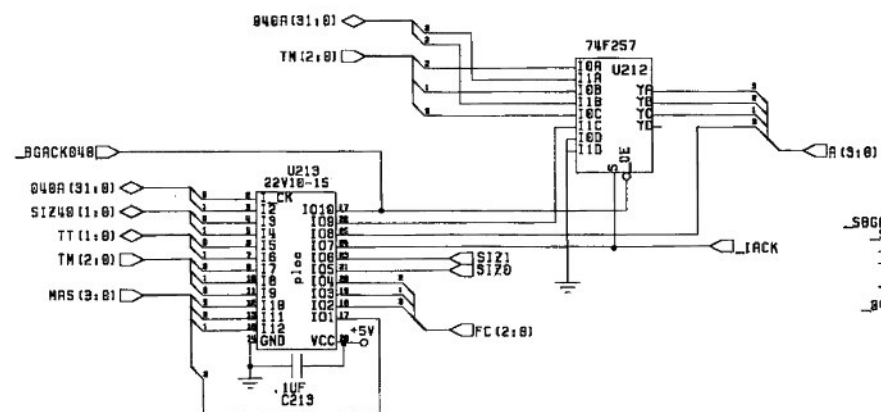
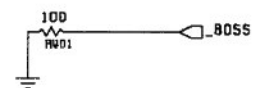
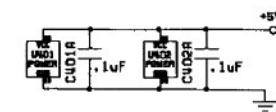
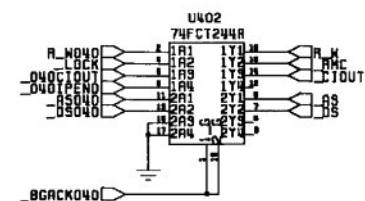
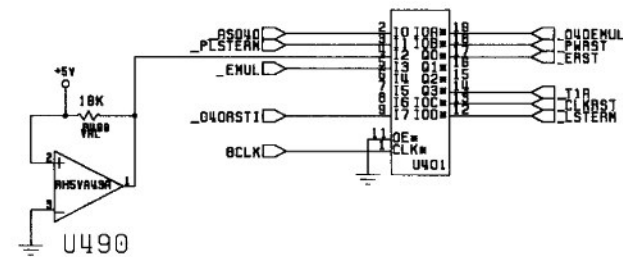
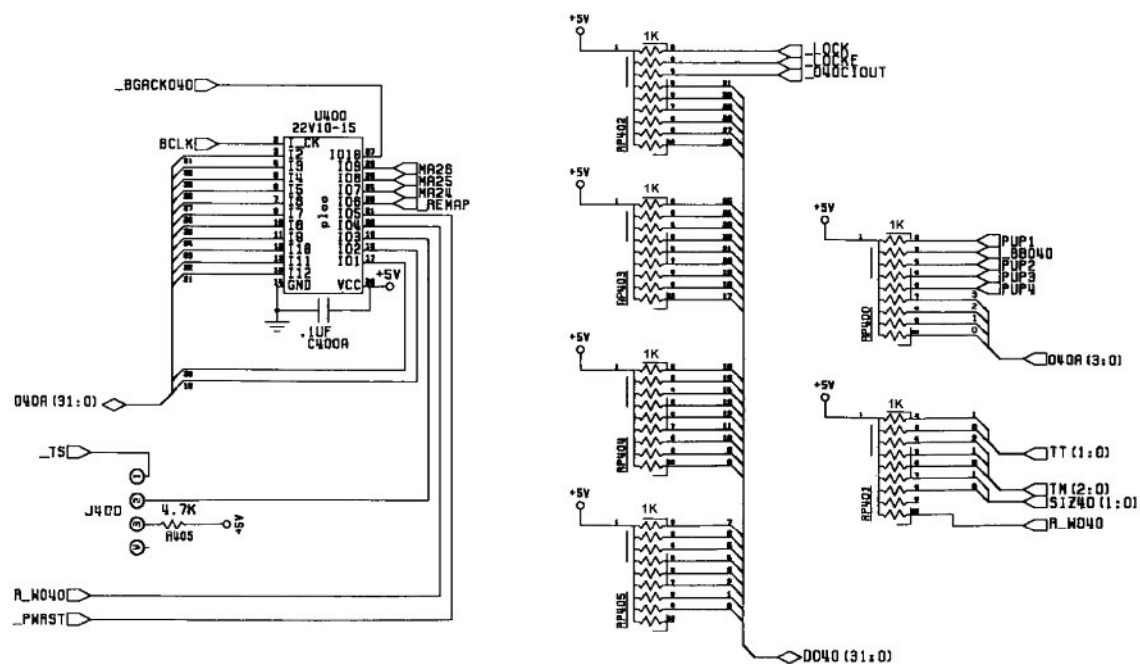
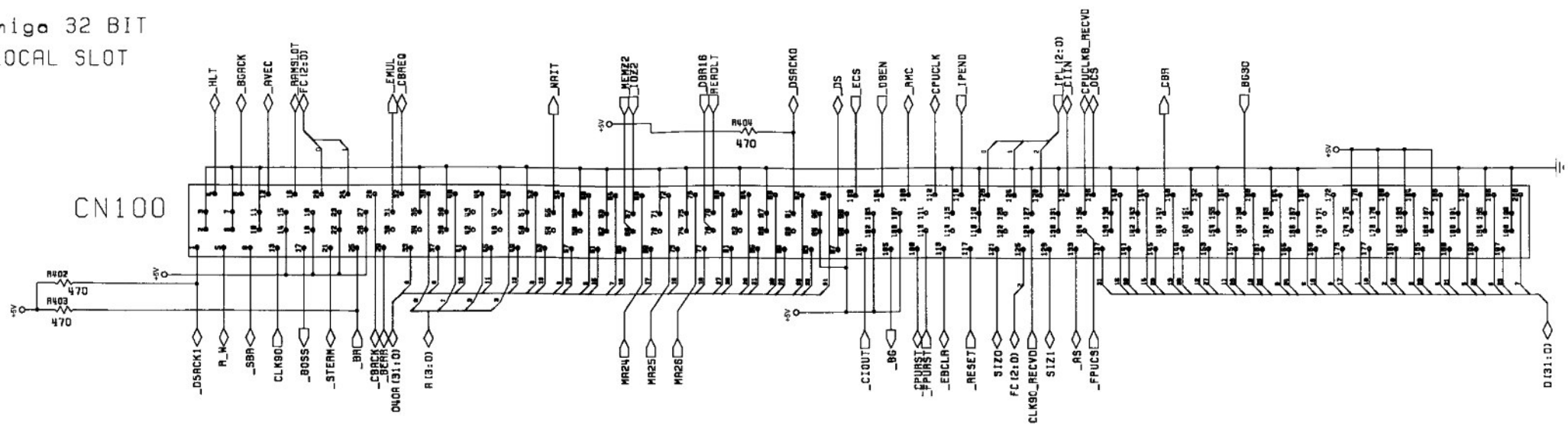


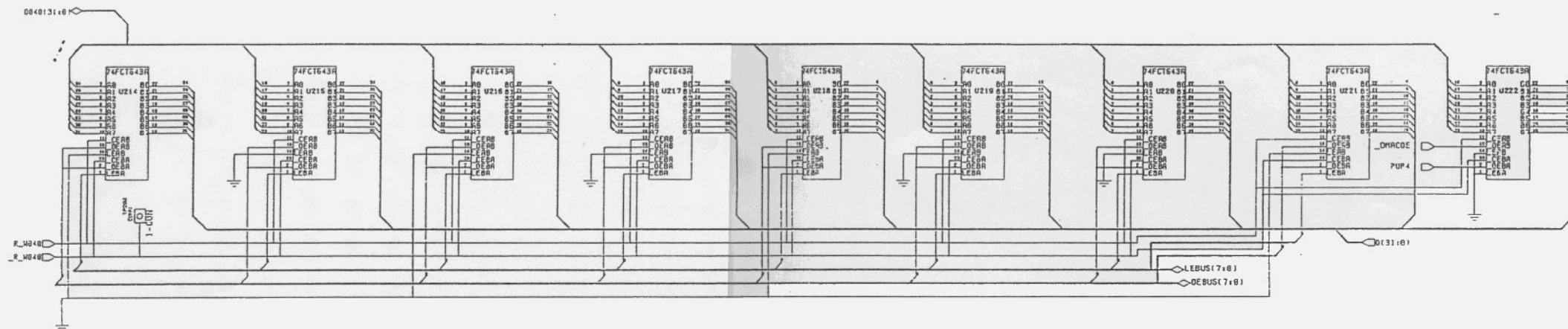


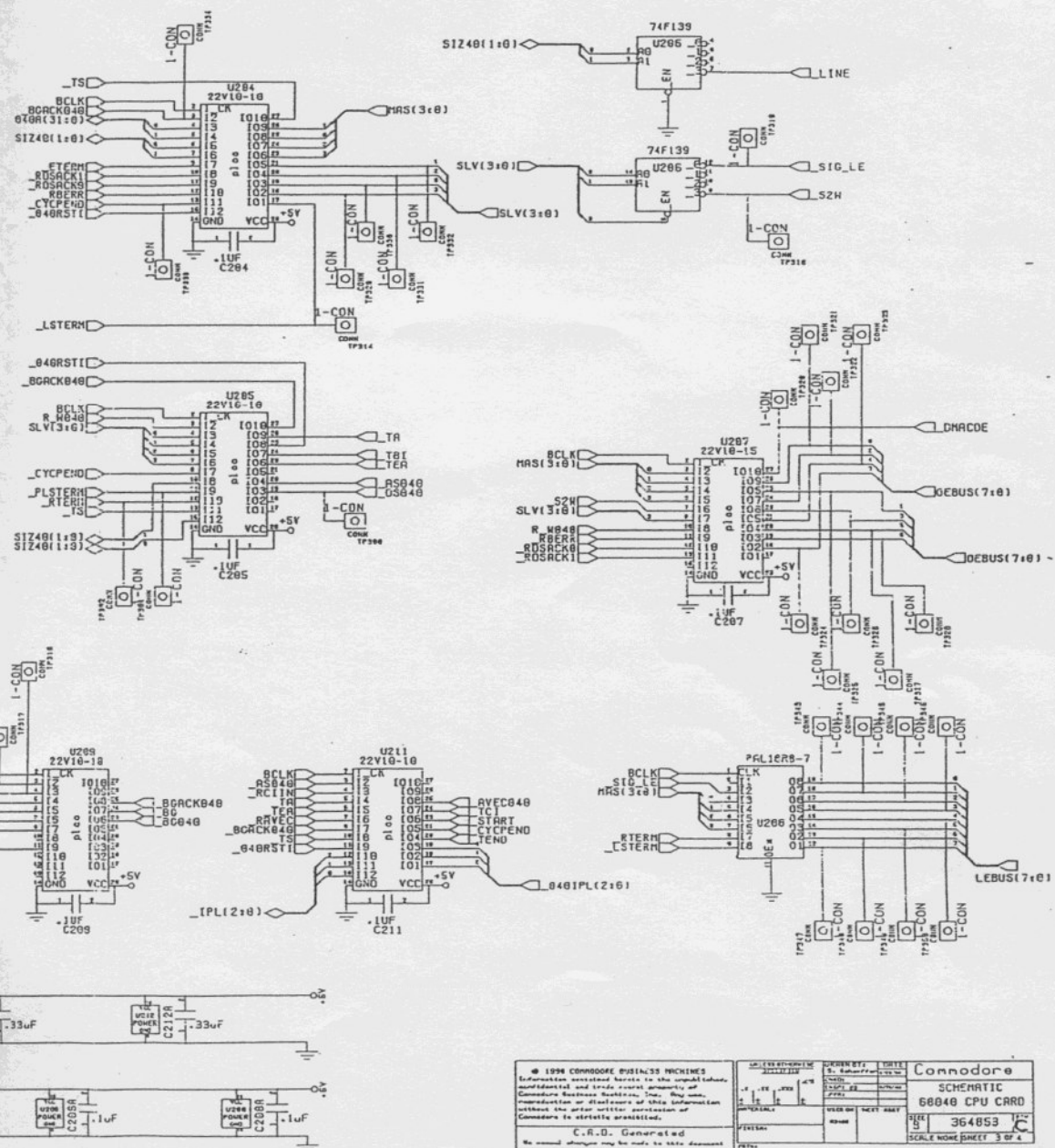
Restored and Edited by Dave Haynie	DRAWN BY:	DATE	Commodore	
	S. Schaeffer	2/25/92		
		2/26/92		
Copyright 1997			SCHEMATIC	
			68040 CPU CARD	
			B	364853
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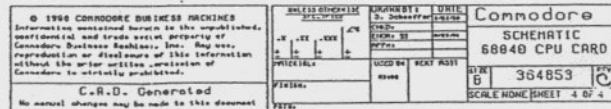


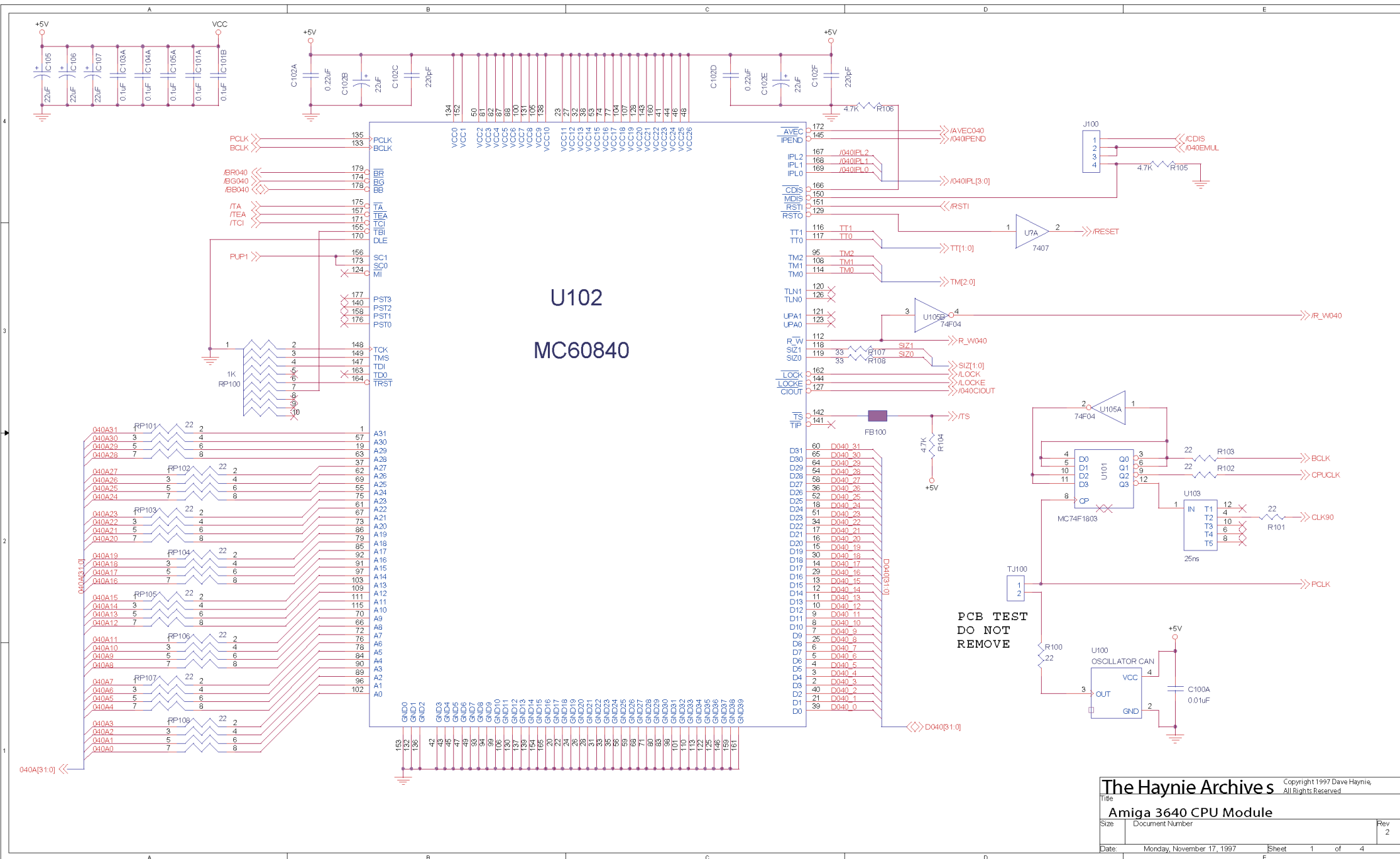
Amiga 32 BIT
LOCAL SLOT

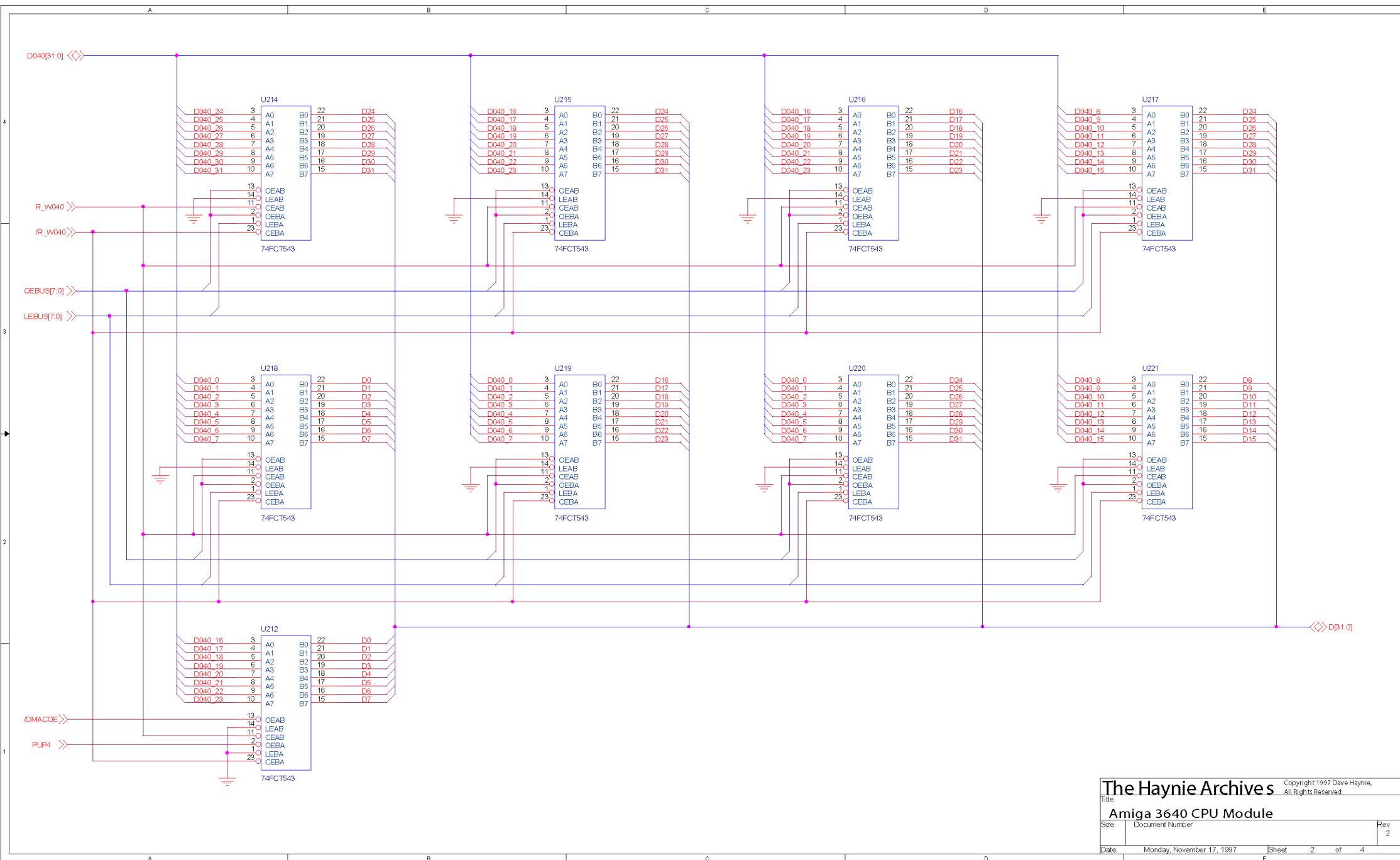


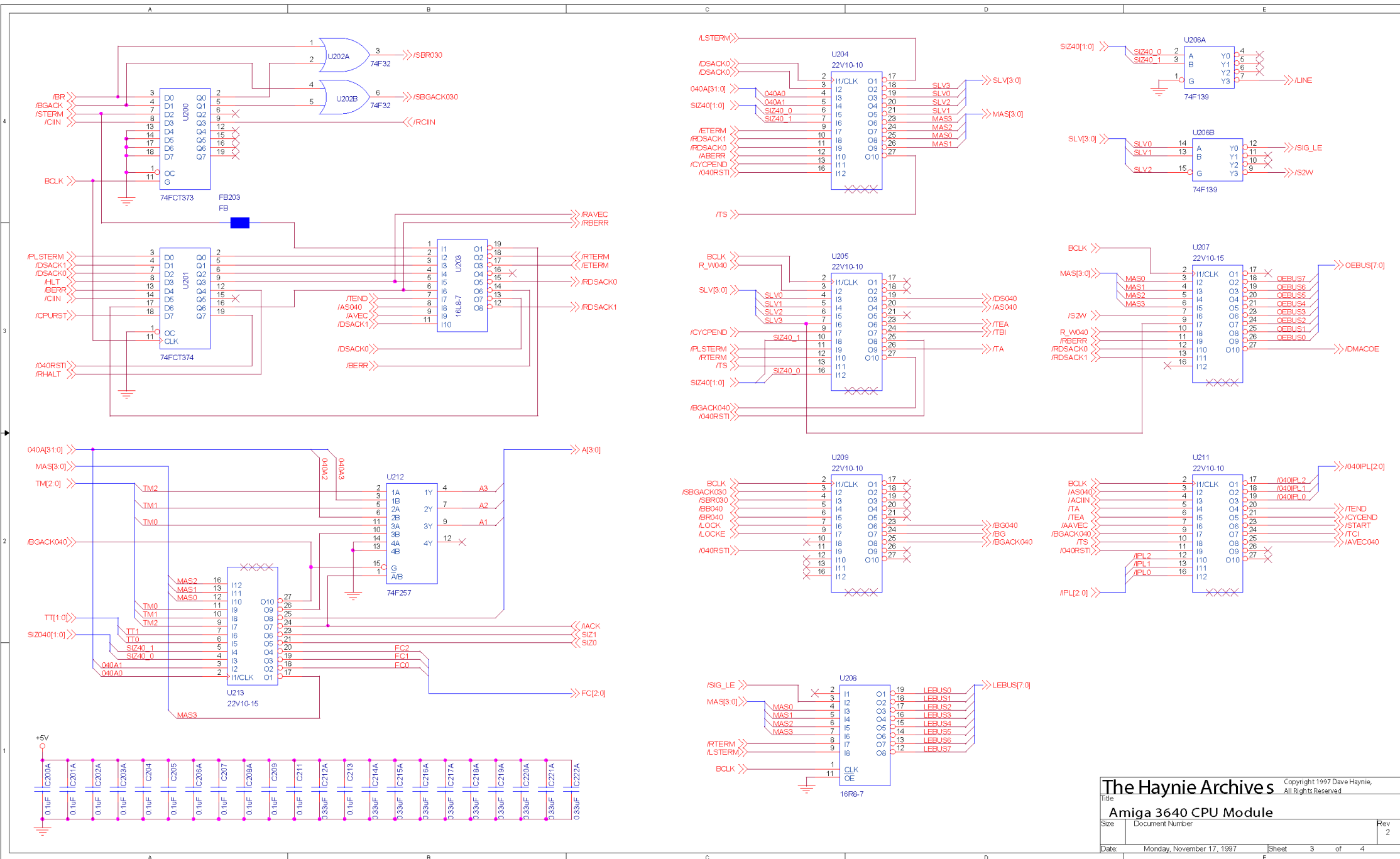


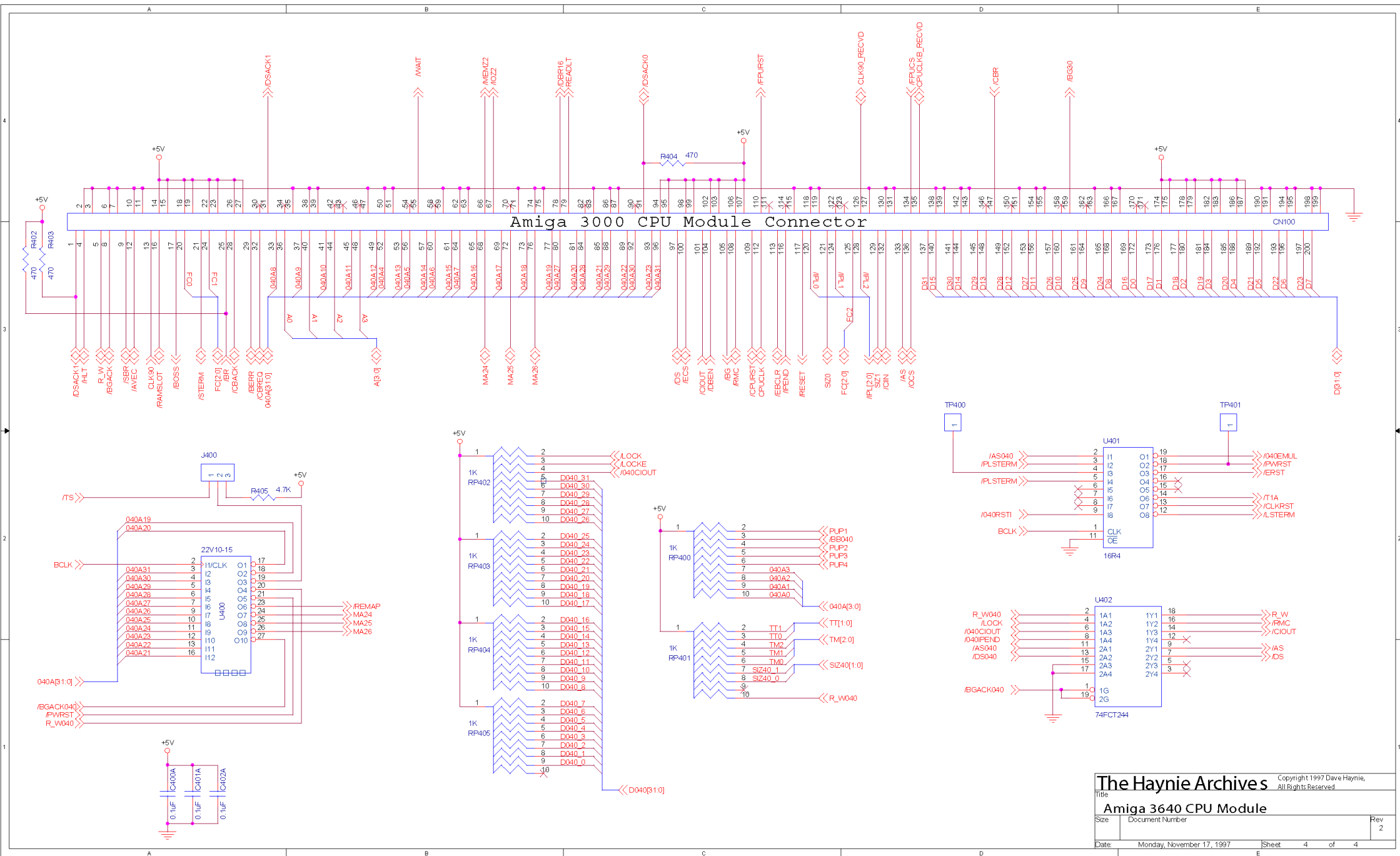
[illegible]

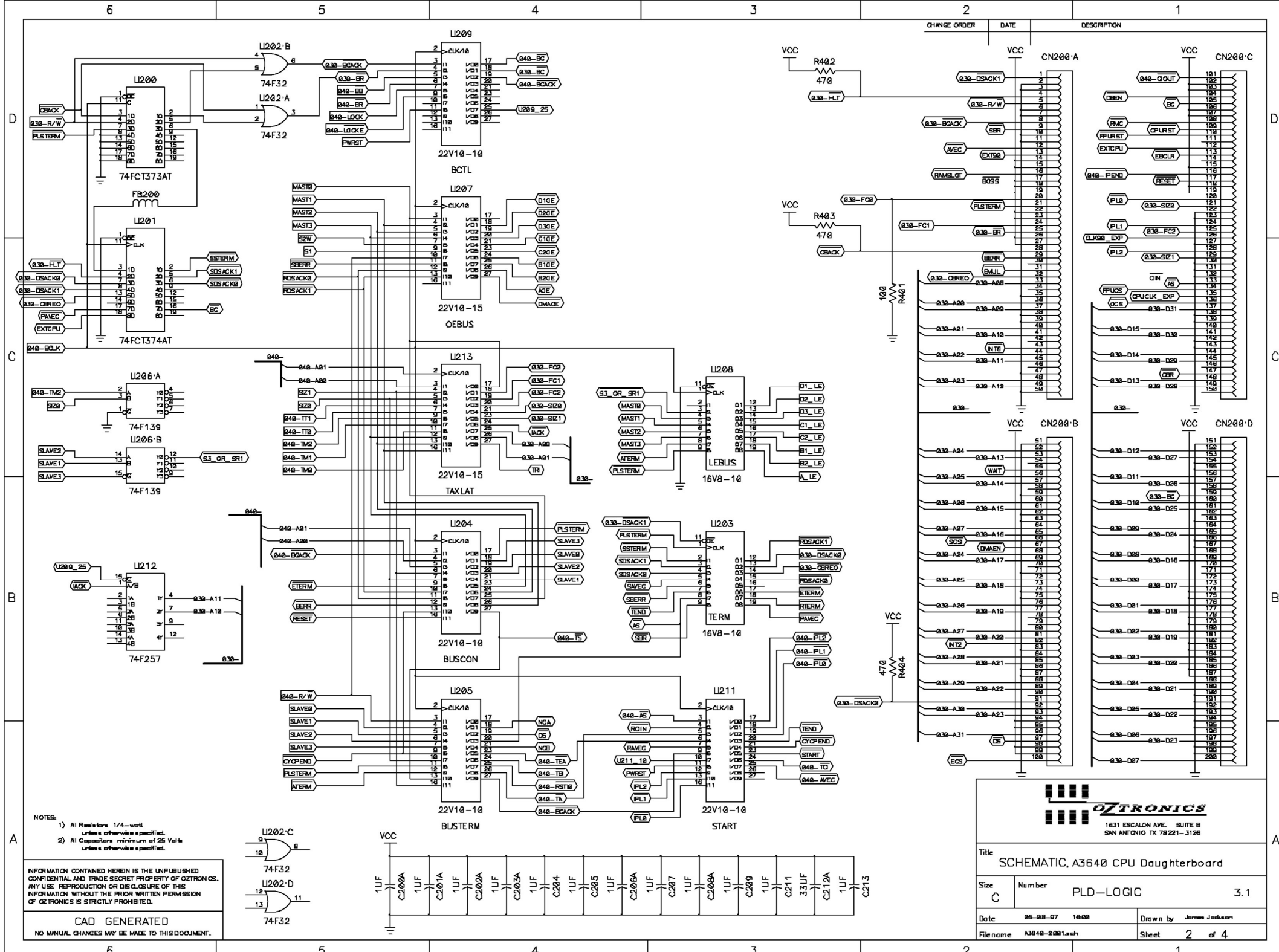


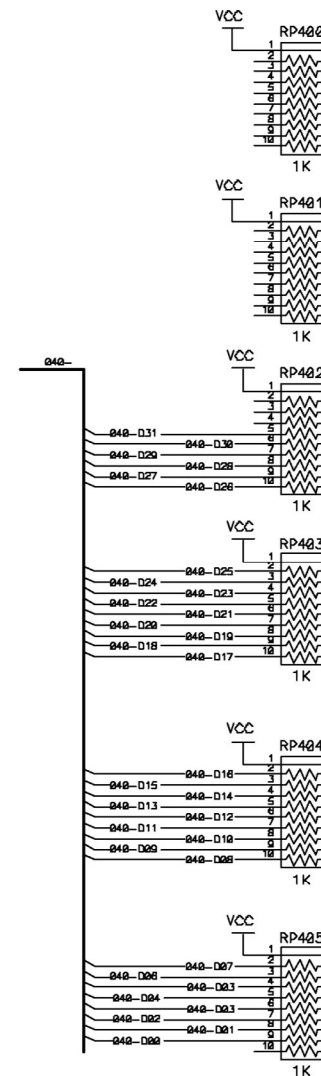












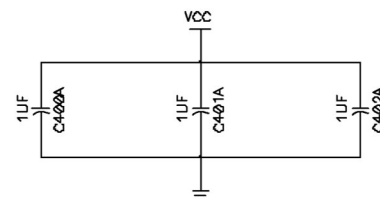
NOTES:

- 1) All Resistors 1/4-watt
unless otherwise specified.
- 2) All Capacitors minimum of 25 Volts
unless otherwise specified.

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Title SCHEMATIC, A3640 CPU Daughterboard

Size	Number	GLUE-LOGIC	3.1
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Date	05-08-97	18:00	Drawn by	James Jackson
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Filename	A3840-2001.sch	Sheet	3	of	4
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